

A Designers Guide To Asynchronous Vlsi

This book is structured as a step-by-step course of study along the lines of a VLSI integrated circuit design project. The entire Verilog language is presented, from the basics to everything necessary for synthesis of an entire 70,000 transistor, full-duplex serializer-deserializer, including synthesizable PLLs. The author includes everything an engineer needs for in-depth understanding of the Verilog language: Syntax, synthesis semantics, simulation and test. Complete solutions for the 27 labs are provided in the downloadable

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files that accompany the book. For readers with access to appropriate electronic design tools, all solutions can be developed, simulated, and synthesized as described in the book. A partial list of design topics includes design partitioning, hierarchy decomposition, safe coding styles, back annotation, wrapper modules, concurrency, race conditions, assertion-based verification, clock synchronization, and design for test. A concluding presentation of special topics includes System Verilog and Verilog-AMS.

JavaScript is the brains of your Web page—it enables you to modify a document ' s structure, styling, and content in response to

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user actions without requesting new pages from the server. Scriptin' with JavaScript and Ajax teaches you how to master this powerful and elegant language so you can develop intuitive user interactions that take the user experience to new levels of sophistication and responsiveness. Today ' s application-like Web experiences (such as Salesforce.com and Google Maps) and Web 2.0 sites (such as Flickr.com and Twitter) are powered by JavaScript and Ajax. Using the techniques shown in this book, you will be able to start creating similar experiences in the sites you design. Scriptin' with JavaScript and Ajax will teach you how to: Start developing with

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JavaScript fast! Write lightweight but powerful object-oriented code
Modify the Document Object Model
“ Progressively enhance ” your pages with JavaScript to provide the highest levels of accessibility to all users
Learn sophisticated techniques for making your pages respond to user actions
Use the downloadable Scriptin ’ library of helper functions to speed development and ensure cross-browser compatibility
Use Ajax scripting techniques to update specific areas of the page with data from the server
Create powerful interface interactions, such as sliding panels and tree menus
Evaluate frameworks such as jQuery and Prototype to find the

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best one for your needs Build an online application that looks and responds like a regular desktop application Easily adapt the Scriptin ' code examples for use in your own projects—download them at www.scriptinwithajax.com Covering database, code, and architecture design for the Oracle operating system, this text is arranged in four sections including an overview of Oracle and data modelling; and aspects of database design including denormalization, data types, nulls, keys and indexes. In the world of optical data communications this book will be an absolute must-read. It focuses on optical communications for short and very short distance applications

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and discusses the monolithic integration of optical receivers with processing elements in standard CMOS technologies. What ' s more, it provides the reader with the necessary background knowledge to fully understand the trade-offs in short-distance communication receiver design and presents the key issues to be addressed in the development of such receivers in CMOS technologies. Moreover, novel design approaches are presented.

“ If you are a young designer entering or contemplating entering the UX field this is a canonical book. If you are an organization that really needs to start grokking UX this book is also for you. ” --

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Chris Bernard, User Experience Evangelist, Microsoft User experience design is the discipline of creating a useful and usable Web site or application—one that ' s easily navigated and meets the needs of both the site owner and its users. But there ' s a lot more to successful UX design than knowing the latest Web technologies or design trends: It takes diplomacy, project management skills, and business savvy. That ' s where this book comes in. Authors Russ Unger and Carolyn Chandler show you how to integrate UX principles into your project from start to finish.

- Understand the various roles in UX design, identify stakeholders, and enlist their support
- Obtain

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consensus from your team on project objectives • Define the scope of your project and avoid mission creep • Conduct user research and document your findings • Understand and communicate user behavior with personas • Design and prototype your application or site • Make your product findable with search engine optimization • Plan for development, product rollout, and ongoing quality assurance

Thoroughly revised and expanded to help readers systematically increase their knowledge and insight about Sigma-Delta Modulators Sigma-Delta Modulators (SDMs) have become one of the best choices for the

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implementation of analog/digital interfaces of electronic systems integrated in CMOS technologies. Compared to other kinds of Analog-to-Digital Converters (ADCs), $\Sigma\Delta$ Ms cover one of the widest conversion regions of the resolution-versus-bandwidth plane, being the most efficient solution to digitize signals in an increasingly number of applications, which span from high-resolution low-bandwidth digital audio, sensor interfaces, and instrumentation, to ultra-low power biomedical systems and medium-resolution broadband wireless communications. Following the spirit of its first edition, *Sigma-Delta Converters: Practical Design Guide*, 2nd Edition takes a comprehensive

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look at SDMs, their diverse types of architectures, circuit techniques, analysis synthesis methods, and CAD tools, as well as their practical design considerations. It compiles and updates the current research reported on the topic, and explains the multiple trade-offs involved in the whole design flow of Sigma-Delta Modulators—from specifications to chip implementation and characterization. The book follows a top-down approach in order to provide readers with the necessary understanding about recent advances, trends, and challenges in state-of-the-art $\Sigma\Delta$ Ms. It makes more emphasis on two key points, which were not treated so deeply in

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the first edition: It includes a more detailed explanation of Ms implemented using Continuous-Time (CT) circuits, going from system-level synthesis to practical circuit limitations. It provides more practical case studies and applications, as well as a deeper description of the synthesis methodologies and CAD tools employed in the design of converters. Sigma-Delta Converters: Practical Design Guide, 2nd Edition serves as an excellent textbook for undergraduate and graduate students in electrical engineering as well as design engineers working on SD data-converters, who are looking for a uniform and self-contained

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reference in this hot topic. With this goal in mind, and based on the feedback received from readers, the contents have been revised and structured to make this new edition a unique monograph written in a didactical, pedagogical, and intuitive style.

VHDL, the IEEE standard hardware description language for describing digital electronic systems, has recently been revised. The Designer's Guide to VHDL has become a standard in the industry for learning the features of VHDL and using it to verify hardware designs. This third edition is the first comprehensive book on the market to address the new features of VHDL-2008. First comprehensive

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book on VHDL to incorporate all new features of VHDL-2008, the latest release of the VHDL standard Helps readers get up to speed quickly with new features of the new standard Presents a structured guide to the modeling facilities offered by VHDL Shows how VHDL functions to help design digital systems Includes extensive case studies and source code used to develop testbenches and case study examples Helps readers gain maximum facility with VHDL for design of digital systems
Is this book for programmers?
Written specifically for graphic designers and production artists already comfortable working with Adobe InDesign, this book teaches

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you how to automate publishing without learning a lot of scary code. XML simplifies the process of moving content in and out of your layouts and can speed up any print or Web assignment. Why should I care about XML? If you 're managing data-intensive layout projects, and you want to keep that data consistent, accurate, and up-to-date, then incorporating XML can help. You can also use XML to automate processes like importing text and large numbers of graphics into a layout, or repurposing content from one application to another. Do I need additional plugins or special software? You need only the powerful features built into InDesign CS2, CS3 or CS4 to use

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this book. How will I learn XML?
This guide includes nine easy-to-follow projects with downloadable support files. With these hands-on tutorials, you will learn XML in context. By the final project, XML will no longer be a mystery, but a powerful tool you can use to support your company or clients. These real-world projects will teach you:

- | Business cards: Create a structured layout in InDesign and then import XML. Format text automatically and flow the text into multiple examples.
- | Product catalog: Learn how to import XML data into a structured layout using nested styles and cloned text.
- | Direct mail and variable data printing: Use XML and InDesign to

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create sophisticated direct mail pieces without costly plug-ins or third-party software. | Magazine and newspaper: Build and export magazine-style publications for online distribution. | Web: Transform XML into XHTML to incorporate directly into your Web pages, or use Cascading Style Sheets to instantly format your exported XML.

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EN 1998-5 Eurocode 8](#)

[The Designer's Guide to the Cortex-
M Processor Family](#)

[A Designer's Guide to VHDL](#)

Synthesis is intended for both design engineers who want to use VHDL-based logic synthesis ASICs and for managers who need to gain a practical understanding of the issues involved in using this technology.

The emphasis is placed more on

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practical applications of VHDL and synthesis based on actual experiences, rather than on a more theoretical approach to the language. VHDL and logic synthesis tools provide very powerful capabilities for ASIC design, but are also very complex and represent a radical departure from traditional design methods. This situation has made it difficult to get started in using this technology for both designers and management, since a major learning effort and 'culture' change is required. A Designer's Guide to VHDL Synthesis has been written to help design engineers and other professionals successfully make the transition to a design methodology based on VHDL and logic synthesis instead of the more traditional schematic based approach. While

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there are a number of texts on the VHDL language and its use in simulation, little has been written from a designer's viewpoint on how to use VHDL and logic synthesis to design real ASIC systems. The material in this book is based on experience gained in successfully using these techniques for ASIC design and relies heavily on realistic examples to demonstrate the principles involved.

If you are a designer who knows HTML, CSS, and JavaScript, you can easily learn how to make native iPhone, iPod touch, and iPad apps—and distribute them worldwide via the App Store. When combined with an Objective-C framework, web standards can be used to format and style content for iOS, using native controls and behaviors for an

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authentic Apple user experience. The Web Designer's Guide to iOS Apps shows how to create apps using NimbleKit, the Objective-C framework featured on the Apple Development Tools web site. With it you'll learn how to: Download and install the free software for creating iOS apps Use Xcode to manage project assets, test apps, and package binary files Implement standard iOS elements and behaviors for familiar Apple mobile user experiences Enhance your app designs with HTML5 and CSS3 Build a foundation for designing web apps that can be delivered to other smartphones and mobile devices This special edition of the Educational Communications and Technology Yearbook Series bears a title of "Learning Environment and

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Design: Current and Future Impact". It provides a timely forum to share theoretical and practical insights in both the local and international contexts in response to the fact that new media and technologies have infiltrated and shaped the learning environments from mere physical spaces into multifaceted possibilities, impacting the ways individuals teach and learn. Designs of learning environments to harness technologies appropriately to engage learners better, as well as the roles of learners and educators play in this changing learning environment, are examples of important global issues in the discourse of the contemporary educational developments. Having gathered a diverse collection of research papers written by scholars and practitioners in the fields of

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education, communication and humanities across Asia, Australasia, Europe and the United States, this book gives readers a cross-cultural background on the developments of technological designs and educational practices, investigating areas in redefining of quality education; online learning and blended learning; new media in education; gamification, AI, and innovative learning technologies. Aimed to catalyze knowledge exchanges and provide fresh views on interdisciplinary research, the book sheds light on how emerging technologies can be adapted in the fields of education and communication, so as to facilitate the current and future designs of learning environments to improve learners' performances.

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The Designer's Guide to the Cortex-M Microcontrollers gives you an easy-to-understand introduction to the concepts required to develop programs in C with a Cortex-M based microcontroller. The book begins with an overview of the Cortex-M family, giving architectural descriptions supported with practical examples, enabling you to easily develop basic C programs to run on the Cortex-M0/M0+/M3 and M4 and M7. It then examines the more advanced features of the Cortex architecture such as memory protection, operating modes, and dual stack operation. Once a firm grounding in the Cortex-M processor has been established the book introduces the use of a small footprint RTOS and the CMSIS-DSP library. The book also examines

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techniques for software testing and code reuse specific to Cortex-M microcontrollers. With this book you will learn: the key differences between the Cortex-M0/M0+/M3 and M4 and M7; how to write C programs to run on Cortex-M based processors; how to make the best use of the CoreSight debug system; the Cortex-M operating modes and memory protection; advanced software techniques that can be used on Cortex-M microcontrollers; how to use a Real Time Operating System with Cortex-M devices; how to optimize DSP code for the Cortex-M4; and how to build real time DSP systems. Includes an update to the latest version (5) of MDK-ARM, which introduces the concept of using software device packs and software components Includes

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overviews of the new CMSIS specifications Covers developing software with CMSIS-RTOS showing how to use RTOS in a real world design Provides a new chapter on the Cortex-M7 architecture covering all the new features Includes a new chapter covering test driven development for Cortex-M microcontrollers Features a new chapter on creating software components with CMSIS-Pack and device abstraction with CMSIS-Driver Features a new chapter providing an overview of the ARMv8-M architecture including the TrustZone hardware security model Behavioral Synthesis: A Practical Guide to High-Level Design includes details on new material and new interpretations of old material with an emphasis on practical

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information. The intended audience is the ASIC (or high-end FPGA) designer who will be using behavioral synthesis, the manager who will be working with those designers, or the engineering student who is studying leading-edge design techniques. Today's designs are creating tremendous pressures for digital designers. Not only must they compress more functionality onto a single IC, but this has to be done on shorter schedules to stay ahead in extremely competitive markets. To meet these opposing demands, designers must work at a new, higher level of abstraction to efficiently make the kind of architectural decisions that are critical to the success of today's complex designs. In other words, they must include behavioral design

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in their flow. The biggest challenge to adopting behavioral design is changing the mindset of the designer. Instead of describing system functionality in great detail, the designer outlines the design in broader, more abstract terms. The ability to easily and efficiently consider multiple design alternatives over a wide range of cost and performance is an extremely persuasive reason to make this leap to a high level of abstraction. Designers that learn to think and work at the behavioral level will reap major benefits in the resultant quality of the final design. But such changes in methodology are difficult to achieve rapidly. Education is essential to making this transition. Many designers will recall the difficulty transitioning from

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schematic-based design to RTL design. Designers that were new to the technology often felt that they had not been told enough about how synthesis worked and that they were not taught how to effectively write HDL code that would synthesize efficiently. Using this unique book, a designer will understand what behavioral synthesis tools are doing (and why) and how to effectively describe their designs that they are appropriately synthesized. CD ROM INCLUDED! The accompanying CD-ROM contains the source code and test benches for the three case studies discussed in Chapters 14, 15 and 16.

Hardware Design and Petri Nets presents a summary of the state of the art in the applications of Petri nets to designing digital systems and

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circuits. The area of hardware design has traditionally been a fertile field for research in concurrency and Petri nets. Many new ideas about modelling and analysis of concurrent systems, and Petri nets in particular, originated in theory of asynchronous digital circuits. Similarly, the theory and practice of digital circuit design have always recognized Petri nets as a powerful and easy-to-understand modelling tool. The ever-growing demand in the electronic industry for design automation to build various types of computer-based systems creates many opportunities for Petri nets to establish their role of a formal backbone in future tools for constructing systems that are increasingly becoming distributed, concurrent and asynchronous. Petri nets have already proved very

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effective in supporting algorithms for solving key problems in synthesis of hardware control circuits. However, since the front end to any realistic design flow in the future is likely to rely on more pragmatic Hardware Description Languages (HDLs), such as VHDL and Verilog, it is crucial that Petri nets are well interfaced to such languages. Hardware Design and Petri Nets is divided into five parts, which cover aspects of behavioral modelling, analysis and verification, synthesis from Petri nets and STGs, design environments based on high-level Petri nets and HDLs, and finally performance analysis using Petri nets. Hardware Design and Petri Nets serves as an excellent reference source and may be used as a text for advanced courses on the subject.

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This guide is for anyone with an interest in helping others to learn. You may be a teacher, trainer, lecturer or coach. You may be a subject expert with knowledge you want to share or an experienced practitioner who wants to pass on their tips. You may already be a creator of learning content, looking to update their skills. Whatever your interest, this guide will help you to design learning materials that really make a difference. Digital learning content takes a wide variety of forms, including tutorials, scenarios, podcasts, screencasts, videos, slideshows, quizzes and reference materials. This guide provides you with fundamental principles that you can apply to any content creation activity as well as practical information relating to specific

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content types. We are fast approaching a point where all learning content will be digital and online. It's time to join the revolution, to contribute as much as you consume. Your learning journey starts here.

Presents languages and notation systems of ID and the integration of these technologies in education.

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[Rapid Instructional Design](#)

[CAAD futures 1997](#)

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Learning Environment
Modeling, or LEM,
provides a simple system
for designing learning
environments that uses
an easy-to-understand
language combined with a

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visual modeling process. LEM is used for improving understanding, decision-making, and communication within learning environment design experiences.

Create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design.

Since the establishment of the CAAD futures Foundation in 1985 CAAD experts from all over the world meet every two years to present and at

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the same time document the state of art of research in Computer Aided Architectural Design. The history of CAAD futures started in the Netherlands at the Technical Universities of Eindhoven and Delft, where the CAAD futures Foundation came into being. Then CAAD futures crossed the oceans for the first time, the third CAAD futures in 1989 was held at Harvard University. Next stations in the evolution were in 1991

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Swiss Federal Institute of Technology, the ETC, Zürich. In 1993 the conference was organized by Carnegie Mellon University, Pittsburgh and in 1995 by National University, Singapore, CAAD futures 1995 marked the world wide nature by organizing it for the first time in Asia. Proceedings of CAAD futures held biannually provide a complete review of the state of research in Computer Aided Architectural Design.

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This book constitutes the refereed proceedings of the 22nd International Conference on Integrated Circuit and System Design, PATMOS 2012, held in Newcastle, UK Spain, in September 2012. The 25 revised full papers presented were carefully reviewed and selected from numerous submissions. The paper feature emerging challenges in methodologies and tools for the design of upcoming generations of

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integrated circuits and systems, including reconfigurable hardware such as FPGAs. The technical program focus on timing, performance and power consumption as well as architectural aspects with particular emphasis on modeling, design, characterization, analysis and optimization. Construction researchers and industry practitioners have begun to explore the possibilities offered by

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mobile and pervasive computing in architecture, engineering and construction (AEC). It is expected that the construction industry will be keen to apply these technologies as they promise significant benefits in areas such as materials management, project management, distributed collaboration and information management, all leading to improvements in productivity. This book

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offers a comprehensive reference volume to the use of mobile and pervasive computing in construction. Based on contributions from a mix of leading researchers and experts from academia and industry, it provides up-to-date insights into current research topics in this field as well as the latest technological advancements and practical examples. The chapters introduce the key theoretical concepts in mobile and pervasive

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computing and highlight the applications and solutions which are available to the construction industry. More specifically, the book focuses on the manner in which these technologies can be applied to improve practices in construction and related industries. This book will be of particular interest to academics, researchers, and graduate students at universities and industrial practitioners

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seeking to apply mobile and pervasive computing systems to improve construction industry productivity.

This book contains extended and revised versions of the best papers presented at the 20th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2012, held in Santa Cruz, CA, USA, in October 2012. The 12 papers included in the book were carefully reviewed and selected

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from the 33 full papers presented at the conference. The papers cover a wide range of topics in VLSI technology and advanced research. They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system-design levels, as well as in the test of these systems.

Field Programmable Gate

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Arrays (FPGAs) are devices that provide a fast, low-cost way for embedded system designers to customize products and deliver new versions with upgraded features, because they can handle very complicated functions, and be reconfigured an infinite number of times. In addition to introducing the various architectural features available in the latest generation of FPGAs, The Design Warrior's Guide to FPGAs also covers

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different design tools and flows. This book covers information ranging from schematic-driven entry, through traditional HDL/RTL-based simulation and logic synthesis, all the way up to the current state-of-the-art in pure C/C++ design capture and synthesis technology. Also discussed are specialist areas such as mixed hardware/software and DSP-based design flows, along with innovative new devices such as field

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programmable node arrays (FPNAs). Clive "Max" Maxfield is a bestselling author and engineer with a large following in the electronic design automation (EDA) and embedded systems industry. In this comprehensive book, he covers all the issues of interest to designers working with, or contemplating a move to, FPGAs in their product designs. While other books cover fragments of FPGA technology or

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applications this is the first to focus exclusively and comprehensively on FPGA use for embedded systems. First book to focus exclusively and comprehensively on FPGA use in embedded designs World-renowned best-selling author Will help engineers get familiar and succeed with this new technology by providing much-needed advice on choosing the right FPGA for any design project
PC Card (or PCMCIA)

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technology allows computers to interface with each other using less space than conventional interfaces. Currently, most applications are in the personal computing market, to enhance peripheral capabilities. As the industry changes, the applications will grow outside of the PC arena, into areas such as medical instrumentation and digital cameras, where peripheral expansion was previously unavailable.

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One of the advantages of this book over others is that it does more than repeat standards or list suppliers. It actually describes and demonstrates design examples which can be applied to projects. This makes it a useful guide design engineers who want to take advantage of the PC Card technology in their work. Faisal Haque is Design Engineering Manager at Baynetworks in Santa Clara, California and has been

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involved in PCMCIA design for the past four years. He is currently the chair of the PC Card ATA Working Group and has contributed to the 1995 PC Card Standard. A designer's guide to PC Card (PCMCIA). Design and software implementation examples. Coverage includes Release 2.1 as well as PC Card'95.

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on Very Large Scale
Integration, VLSI-SoC
2012, Santa Cruz, CA,
USA, October 7-10, 2012,
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Valley Polytechnic
Institute
Learning ID Fast and
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Create iPhone, iPod
touch, and iPad apps
with Web Standards
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JavaScript)
Programmable Logic
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As online courses and digital learning enable more people from more places to learn together, it is crucial for instructional design to incorporate diverse cultural

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perspectives. Culturally Inclusive Instructional Design provides a framework for thinking about culture in digital learning, offering insight into how to build inclusive online communities that encourage reflection and growth, regardless of content domain. Chapters cover the foundation, components, and implementation of the authors' Wisdom Communities (WisCom) framework, which enables learners from global backgrounds to experience long-lasting, transformative

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learning through real-world problem-solving. This book is a timely, resourceful guide to building truly collaborative, inquiry-based online learning experiences.

While making up a larger percentage of the total number of designs produced each year, ASICs present special problems for system designers in the area of testing because each design is complex and unique. This book shows readers how to apply basic test techniques to ASIC design, details the impact o

This book constitutes the

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*refereed proceedings of the
21st International
Conference on Integrated
Circuit and System Design,
PATMOS 2011, held in
Madrid, Spain, in September
2011. The 34 revised full
papers presented were
carefully reviewed and
selected from numerous
submissions. The paper
feature emerging challenges
in methodologies and tools
for the design of upcoming
generations of integrated
circuits and systems and
focus especially on timing,
performance and power
consumption as well as*

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architectural aspects with particular emphasis on modeling, design, characterization, analysis and optimization.

Create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design. This practical alternative to conventional synchronous design enables performance close to full-custom designs with design times that approach commercially available ASIC standard cell flows. It includes design trade-offs, specific design

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examples, and end-of-chapter exercises. Emphasis throughout is placed on practical techniques and real-world applications, making this ideal for circuit design students interested in alternative design styles and system-on-chip circuits, as well as circuit designers in industry who need new solutions to old problems. A recent technological advance is the art of designing circuits to test themselves, referred to as a Built-In Self-Test. This book is written from a designer's perspective and describes

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the major BIST approaches that have been proposed and implemented, along with their advantages and limitations.

This series of Designers Guides to the Eurocodes provides comprehensive guidance in the form of design aids, indications for the most convenient design procedures and worked examples. All of the individual guides work in conjunction with the Designers' Guide to EN1990 Eurocode: Basis of Structural Design.

Engineering productivity in

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integrated circuit product design and development today is limited largely by the effectiveness of the CAD tools used. For those domains of product design that are highly dependent on transistor-level circuit design and optimization, such as high-speed logic and memory, mixed-signal analog-digital interfaces, RF functions, power integrated circuits, and so forth, circuit simulation is perhaps the single most important tool. As the complexity and performance of integrated electronic systems has

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increased with scaling of technology feature size, the capabilities and sophistication of the underlying circuit simulation tools have correspondingly increased. The absolute size of circuits requiring transistor-level simulation has increased dramatically, creating not only problems of computing power resources but also problems of task organization, complexity management, output representation, initial condition setup, and so forth. Also, as circuits of more complexity and mixed types of

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functionality are attacked with simulation, the spread between time constants or event time scales within the circuit has tended to become wider, requiring new strategies in simulators to deal with large time constant spreads.

With asynchronous circuit design becoming a powerful tool in the development of new digital systems, circuit designers are expected to have asynchronous design skills and be able to leverage them to reduce power consumption and increase system speed. This book

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walks readers through all of the different methodologies of asynchronous circuit design, emphasizing practical techniques and real-world applications instead of theoretical simulation. The only guide of its kind, it also features an ftp site complete with support materials.

Market: Electrical Engineers, Computer Scientists, Device Designers, and Developers in industry. An Instructor Support FTP site is available from the Wiley editorial department.

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respond to this design challenge, the industry has developed and standardized VHDL-AMS, a unified design language for modeling digital, analog, mixed-signal, and mixed-technology systems. VHDL-AMS extends VHDL to bring the successful HDL modeling methodology of digital electronic systems design to these new design disciplines. Gregory Peterson and Darrell Teegarden join best-selling author Peter Ashenden in teaching designers how to use VHDL-AMS to model these complex

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systems. This comprehensive tutorial and reference provides detailed descriptions of both the syntax and semantics of the language and of successful modeling techniques. It assumes no previous knowledge of VHDL, but instead teaches VHDL and VHDL-AMS in an integrated fashion, just as it would be used by designers of these complex, integrated systems. Explores the design of an electric-powered, unmanned aerial vehicle system (UAV) in five separate case studies

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to illustrate mixed-signal, mixed-technology, power systems, communication systems, and full system modeling. The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient, portable, and scalable Register Transfer Level (RTL) digital circuits using the VHDL hardware description language and synthesis software. Focusing on the module-level design, which is composed of functional

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*units, routing circuit, and storage, the book illustrates the relationship between the VHDL constructs and the underlying hardware components, and shows how to develop codes that faithfully reflect the module-level design and can be synthesized into efficient gate-level implementation. Several unique features distinguish the book: **

- Coding style that shows a clear relationship between VHDL constructs and hardware components **
- Conceptual diagrams that*

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*illustrate the realization of VHDL codes * Emphasis on the code reuse * Practical examples that demonstrate and reinforce design concepts, procedures, and techniques * Two chapters on realizing sequential algorithms in hardware * Two chapters on scalable and parameterized designs and coding * One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis, it also examines the synthesis task*

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from the perspective of the overall development process. Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation, verification, and testing needs, and can be easily incorporated into a larger system or reused. Discussion is independent of technology and can be applied to both ASIC and FPGA devices. With a balanced presentation of fundamentals and practical examples, this is an excellent textbook for

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upper-level undergraduate or graduate courses in advanced digital logic. Engineers who need to make effective use of today's synthesis software and FPGA devices should also refer to this book.

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